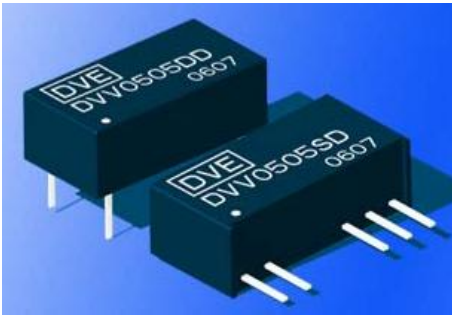




**DVV Series**  
**3kVDC Isolated 1W Dual Output DC-DC Converters**



**FEATURES**

- High Efficiency up to 81 %
- Positive & Negative Voltage Output
- Small Footprint
- 7PIN SIP/14 PIN DIP Package
- Industry Standard Pinout
- UL94-V0 Package
- No Heat sink Required
- 3kVDC Isolation
- Power Density 0.85W/cm<sup>3</sup>
- Temperature Range: -40°C~+85°C
- No External Component Required
- MTTF>3,500,000 hours

**APPLICATIONS**

The DVV Series are specially designed for applications where a group of polar power supplies are isolated from the input power supply in a distributed power supply system on a circuit board.

These products apply to:

- 1) Where the voltage of the input power supply is fixed (voltage variation  $\leq \pm 10\%$ );
- 2) Where isolation is necessary between input and output (isolation voltage =3000VDC);
- 3) Where the regulation of the output voltage and the output ripple noise are not demanding.

Such as: purely digital circuits, ordinary low frequency analog circuits, and IGBT power device driving circuits.

SELECTION GUIDE						
	Nominal Input Voltage	Rated Output Voltage	Output Current		Efficiency	Package Style
			Min	Max		
Order Code	(V)	(V)	(mA)	(mA)	(%,Typ)	
DVV0505DD/SD	5	±5	±10	±100	72	DIP/SIP
DVV0509DD/SD	5	±9	±6	±56	75	DIP/SIP
DVV0512DD/SD	5	±12	±5	±42	78	DIP/SIP
DVV0515DD/SD	5	±15	±4	±33	79	DIP/SIP
DVV1205DD/SD	12	±5	±10	±100	74	DIP/SIP
DVV1209DD/SD	12	±9	±6	±56	76	DIP/SIP
DVV1212DD/SD	12	±12	±5	±42	79	DIP/SIP
DVV1215DD/SD	12	±15	±4	±33	80	DIP/SIP
DVV2405DD/SD	24	±5	±10	±100	74	DIP/SIP
DVV2409DD/SD	24	±9	±6	±56	76	DIP/SIP
DVV2412DD/SD	24	±12	±5	±42	80	DIP/SIP
DVV2415DD/SD	24	±15	±4	±33	81	DIP/SIP
DVV4805DD/SD	48	±5	±10	±100	72	DIP/SIP
DVV4809DD/SD	48	±9	±6	±56	74	DIP/SIP
DVV4812DD/SD	48	±12	±5	±42	78	DIP/SIP
DVV4815DD/SD	48	±15	±4	±33	79	DIP/SIP

INPUT CHARACTERISTICS					
Parameter	Conditions	MIN	TYP	MAX	Units
Voltage Range	All DVV05 Types	4.5	5	5.5	VDC
	All DVV12 Types	10.8	12	13.2	
	All DVV24 Types	21.6	24	26.4	
	All DVV48 Types	43.2	48	52.8	

OUTPUT CHARACTERISTICS					
Parameter	Conditions	MIN	TYP	MAX	Units
Output Power		0.1		1	W
Output Voltage Accuracy	See tolerance envelope graph				
Line Regulation	For Vin change of 1%			1.2	%
Load Regulation	10% To 100% full load		10	15	%
Temperature Drift	100% full load			0.03	%/°C
Temperature Rise	full load		15	25	°C
Ripple	B/W=DC to 20MHz		100	150	mV p-p

ABSOLUTE MAXIMUM RATINGS	
Short-circuit protection	1 second
Lead temperature 1.5mm from case for 10 seconds	300°C

1 All specifications measured at TA=25°C, humidity<75%, nominal input voltage and rated output load unless otherwise specified.

2 See below recommended circuits for more details.



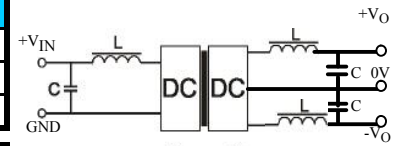
## DVV Series 3kVDC Isolated 1W Dual Output DC-DC Converters



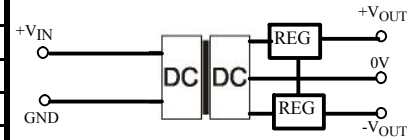
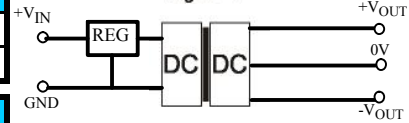
ISOLATION CHARACTERISTICS					
Parameter	Conditions	MIN	TYP	MAX	Units
Isolation Test Voltage	Flash Test for 1 minute	3000			VDC
Resistance	Viso=500VDC	1			GΩ

GENERAL CHARACTERISTICS					
Parameter	Conditions	MIN	TYP	MAX	Units
Switching Frequency	Full load, nominal input voltage		100		kHz

ENVIRONMENTAL					
Parameter	Conditions	MIN	TYP	MAX	Units
Operation		-40		85	°C
Storage temperature		-55		125	°C
Storage humidity				95	%
Cooling	Free air convection				

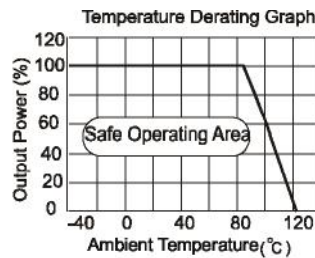
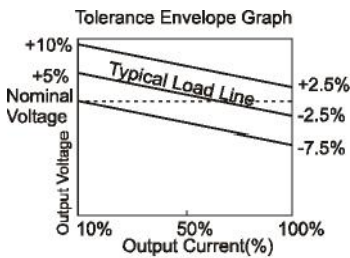


<Figure 1>



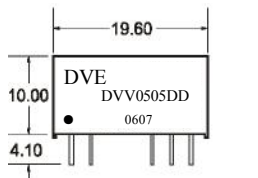
<Figure 2>

### TYPICAL CHARACTERISTICS



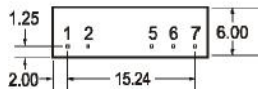
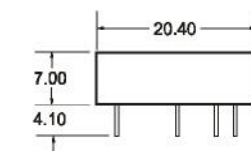
### OUTLINE DIMENSIONS & RECOMMENDED FOOTPRINT

DVV\*\*SD Package

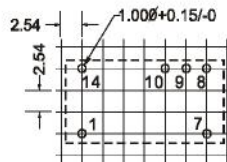
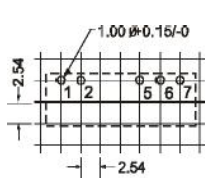
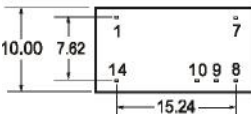


Side View

DVV\*\*DD Package



Bottom View



Note: All Pins on a 2.54mm pitch; All Pin diameters are 0.50 mm; all dimensions in mm.

### APPLICATION NOTE

#### Filtering

In some circuits which are sensitive to noise and ripple, a filtering capacitor may be added to the DC/DC output end and input end to reduce the noise and ripple. However, the capacitance of the output filter capacitor must be proper. If the capacitance is too big, a startup problem might arise. For every channel of output, provided the safe and reliable operation is ensured, the greatest capacitance of its filter capacitor sees the external capacitor table. To get an extremely low ripple, an "LC" filtering network may be connected to the input and output ends of the DC/DC converter, which may produce a more significant filtering effect. It should also be noted that the inductance and the frequency of the "LC" filtering network should be staggered with the DC/DC frequency to avoid mutual interference (see Figure 1).

#### Requirement on output load

To ensure this module can operate efficiently and reliably, a minimum load is specified for this kind of DC/DC converter in addition to a maximum load (namely full load). During operation, make sure the specified range of input voltage is not exceeded, the minimum output load is not less than 10% of the full load, and that this product should never be operated under no load! If the actual output power is very small, please connect a resistor with proper resistance at the output end in parallel to increase the load.

### Overload Protection

Under normal operating conditions, the output circuit of these products has no protection against over-current and short-circuits. The simplest method is to connect a self-recovery fuse in series at the input end or add a circuit breaker to the circuit.

### Output Voltage Regulation and Over-voltage Protection Circuit

The simplest device for output voltage regulation, over-voltage and over-current protection is a linear voltage regulator with overheat protection that is connected to the input or output end in series (see Figure 2).

### External Capacitor Table

V <sub>IN</sub>	External capacitor	V <sub>OUT</sub>	External capacitor
5VDC	4.7μF	5VDC	4.7μF
12VDC	2.2μF	9VDC	2.2μF
24VDC	1μF	12VDC	1μF
		15VDC	0.47μF

### FOOTPRINT DETAILS

DVV\*\*SD

PIN	
1	V <sub>IN</sub>
2	GND
5	-V <sub>O</sub>
6	0V
7	+V <sub>O</sub>

DVV\*\*DD

PIN	
1	GND
7	NC
8	+V <sub>O</sub>
9	0V
10	-V <sub>O</sub>
14	V <sub>IN</sub>